Instructor: Chance Eary  
Office Number: Engineering Research Building, Room 647  
Email Address: chance.eary@uta.edu  
Office Hours: Monday / Wednesday; 7:30am to 8:30am via Microsoft Teams

Section Information:

CSE 2312-003: Monday / Wednesday; Online, Asynchronous

TA: Hehuan Ma  
TA Office Number: Microsoft Teams  
TA Email: hehuan.ma@mavs.uta.edu  
TA Office Hours: Thursday; 4:00pm to 6:00pm

CSE 2312-004: Monday / Wednesday; Online, Asynchronous

TA: Huiyang Li  
TA Office Number: Microsoft Teams  
TA Email: huiyang.li@mavs.uta.edu  
TA Office Hours: Monday / Wednesday; 12:30pm to 2:30pm

Description of Course Content:

This course is designed to provide the student with knowledge of fundamental concepts in computer organization. Individual topics include memory hierarchy, instruction set architectures, memory addressing, input-output, integer and floating-point representation, arithmetic and logic operations, etc. The relationship of higher-level programming languages to the operating system and underlying instruction set architecture will be explored, as well as assembly language programming.

Student Learning Outcomes:

- Range, and size of, integer and Boolean variable types  
- Basis for 2's compliment encoding of signed integers, ALU signed/unsigned agnostic design  
- ALU operating including flag operation  
- ALU register interface in the CPU  
- Arithmetic, logical and shift operations in the ALU  
- Load/store interface between registers and memory  
- Memory addressing modes (direct, indirect, indirect indexed, ...)  
- Flow control instructions and loops in the ALU  
- AAPCS register and calling conventions  
- Writing mixed C / assembly programs  
- Using the GNU compiler, assembler, linker, and debugger  
- Detailed knowledge of ARM arithmetic, logical, load/store, and program flow instructions  
- Effects of packing on performance and memory size  
- Full decrementing stack design and the stack pointer  
- IEEE-754 floating point number range, dynamic range issues, and memory storage  
- Pipelined vs non-pipelined designs  
- Memory virtualization and paging (heap fragmentation, security implications)

Prerequisite: CSE 1320 – Intermediate Programming
Required Materials:

- Raspberry Pi Model 3 B+
  - The CanaKit is strongly recommended: [https://www.amazon.com/CanaKitRaspberry-Complete-Starter-Kit/dp/B01C6Q2G5Y/](https://www.amazon.com/CanaKitRaspberry-Complete-Starter-Kit/dp/B01C6Q2G5Y/)
  - HDMI Monitor, Keyboard, Mouse
  - Students who do not purchase the CanaKit must source the following on their own:
    - Raspberry Pi 3B+ motherboard
    - Power supply
    - Micro-SD card, 16 GB or larger
    - Micro-SD card reader
- Equipment to facilitate the use of Respondus LockDown Browser and Respondus Monitor.
  - Computer with Compatible Operating System
    - Windows 7, 8, or 10
    - MacOS 10.12 or higher
  - Compatible webcam

Recommended Materials:

- Ethernet patch cable
- USB to Ethernet Adapter

Attendance:

Pre-recorded lectures will be posted online via Canvas on the days indicated on the course schedule. Students are responsible for all course material whether or not they choose to view the entirety of all provided lectures.

Communication:

- Course updates will be published via Canvas.
- Students will use their University-provided e-mail to communicate with the instructor outside office hours.
- Students will not use Canvas Messenger to communicate with the instructor.
- Students will not use Microsoft Teams to communicate with the instructor outside office hours.
- E-mails sent to chance.eary@mavs.uta.edu will not be read.
- E-mails received after 4:00pm on a weekday will not receive a response until the next business day.
- E-mails received after 4:00pm on a Friday will not receive a response until the following Monday.
- The instructor will not answer questions about course content while an exam is active.

Programming Lab Policies:

- Labs that fail to compile, or do not terminate correctly, will receive a zero.
- Labs that fail to compile, or do not terminate correctly, may not be resubmitted for a grade. This includes instances where students did not upload the correct file for grading.
- All labs must be submitted via the upload link on Canvas. Labs are not accepted through e-mail or Canvas Messenger.
- **Students must make a credible attempt to pass all programming labs to receive a passing grade in the course.**
Grading:

- Lab 1: 05%
- Lab 2 and 3: 24% (12% each)
- Quizzes: 28% (07% each)
- Test 1: 21%
- Test 2: 22%

- A: 90 – 100
- B: 80 – 89.9
- C: 70 – 79.9
- D: 60 – 69.9
- F: 0 – 59.9

Make-up Exams:

- Students that miss an exam may schedule a make-up exam at a fifty (50) point penalty, plus ten (10) points for every 24 hour delay after an exam closes.
- Exam dates will not be adjusted for any student for any reason.

Grade Grievances:

If a student has questions or concerns about any grade, the student has one week after a grade is posted to raise the issue with the instructor. Grades will not be reexamined after one week.

Late Policy:

For every 24 hour period an assignment is late, 10-points will be deducted from the graded result.

Academic Integrity:

Any student found responsible for an Honor Code violation will receive an F in the course and be referred to the Office of Student Conduct.

Course Schedule – The following course schedule is tentative and is likely to change

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<thead>
<tr>
<th>Date</th>
<th>Day</th>
<th>Topic</th>
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<tbody>
<tr>
<td>20-Jan</td>
<td>Wednesday</td>
<td>Syllabus</td>
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<tr>
<td>25-Jan</td>
<td>Monday</td>
<td>1. Configuring VNC, Calling ASM from C</td>
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<td>27-Jan</td>
<td>Wednesday</td>
<td>2. Registers and Integers</td>
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<td>01-Feb</td>
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<td>3. Flags, Conditionals, Branches</td>
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<td>03-Feb</td>
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<td>08-Feb</td>
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<td>4. Shifts, Masks, 64-bit Values</td>
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<td>10-Feb</td>
<td>Wednesday</td>
<td>5. Patterson and Hennessy – Chapter 1, part 1</td>
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<td>15-Feb</td>
<td>Monday</td>
<td>6. Patterson and Hennessy – Chapter 1, part 2</td>
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<td>17-Feb</td>
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<td>22-Feb</td>
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<td>24-Feb</td>
<td>Wednesday</td>
<td>7. Processor Architecture, ASM without C</td>
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<td>01-Mar</td>
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<td>Date</td>
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<td>03-Mar</td>
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<td>8. Intro to Addresses, Strings</td>
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<td>08-Mar</td>
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<td>9. GDB, Concatenation, Summation</td>
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<td>10-Mar</td>
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<td>15-Mar</td>
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<td>22-Mar</td>
<td>Monday</td>
<td>10. Locality, Memory Hierarchy, Caches</td>
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<td>24-Mar</td>
<td>Wednesday</td>
<td>11. Virtual Machines, Virtual Memory</td>
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<td>29-Mar</td>
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<td>12. Floating Points, VFP</td>
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<td>31-Mar</td>
<td>Wednesday</td>
<td>13. Structs, Padding, Endianness, Capacity</td>
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<td>05-Apr</td>
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<td>07-Apr</td>
<td>Wednesday</td>
<td>14. Procedure Calls, Compiling, and Linking</td>
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<td>12-Apr</td>
<td>Monday</td>
<td>15. Pipelining, Branch Prediction</td>
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<td>14-Apr</td>
<td>Wednesday</td>
<td>16. Dependability, Error Detection and Correction</td>
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<td>19-Apr</td>
<td>Monday</td>
<td>No New Material – Study Day</td>
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<td>21-Apr</td>
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<td>Quiz 4</td>
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<td>26-Apr</td>
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<td>28-Apr</td>
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<tr>
<td>03-May</td>
<td>Monday</td>
<td>Test 2</td>
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**Institution Information:**

UTA students are encouraged to review the below institutional policies and informational sections and reach out to the specific office with any questions. To view this institutional information, please visit the [Institutional Information](https://resources.uta.edu/provost/course-related-info/institutional-policies.php) page which includes the following policies among others:

- Drop Policy
- Disability Accommodations
- Title IX Policy
- Academic Integrity
- Student Feedback Survey
- Final Exam Schedule

**Mandatory Face Covering Policy:**

All students and instructional staff are required to wear facial coverings while they are on campus, inside buildings and classrooms. Students that fail to comply with the facial covering requirement will be asked to leave the class session. If students need masks, they may obtain them at the Central Library, the E.H. Hereford University Center’s front desk or in their department. Students who refuse to wear a facial covering in class will be asked to leave the session by the instructor, and, if the student refuses to leave, they may be reported to UTA’s Office of Student Conduct.

**Student Success Programs:**

UT Arlington provides a variety of resources and programs designed to help students develop academic skills, deal with personal situations, and better understand concepts and information related to their courses. Resources include tutoring by appointment, drop-in tutoring, etutoring, supplemental instruction, mentoring (time management, study skills, etc.), success coaching, TRIO Student Support Services, and student success workshops. For additional information, please email resources@uta.edu, or view the Maverick Resources website.
Emergency Phone Numbers: In case of an on-campus emergency, call the UT Arlington Police Department at 817-272-3003 (non-campus phone), 2-3003 (campus phone). You may also dial 911.